RAHIYANATH T. Y.

- **Assistant Professor** (Bachelor of Computer Application) at The Cochin College, Kochi from August 2022.
- ♣ Pursued M.Tech in Communication Engineering

CORE COMPETENCES

- ♣ A Novel Architecture for Lightweight Block Cipher, Piccolo (Master's Thesis)
- ♣ Security Enhancement on Lightweight Block Cipher, HIGHT (M.Tech Mini Project)
- ♣ Programmable Power Supply (B.Tech Main Project)
- ♣ Zigbee Based Trolley Cart System (B.Tech Mini Project)

ACHIEVEMENTS

- ♣ Secured Score of 405 with 21129th rank among 256135 aspirants in GATE' 13.
- ♣ Published a paper on "A novel architecture for lightweight block cipher, Piccolo" in International Journal of Research in Engineering and Technology, Volume 4, Issue 9; September 2015.